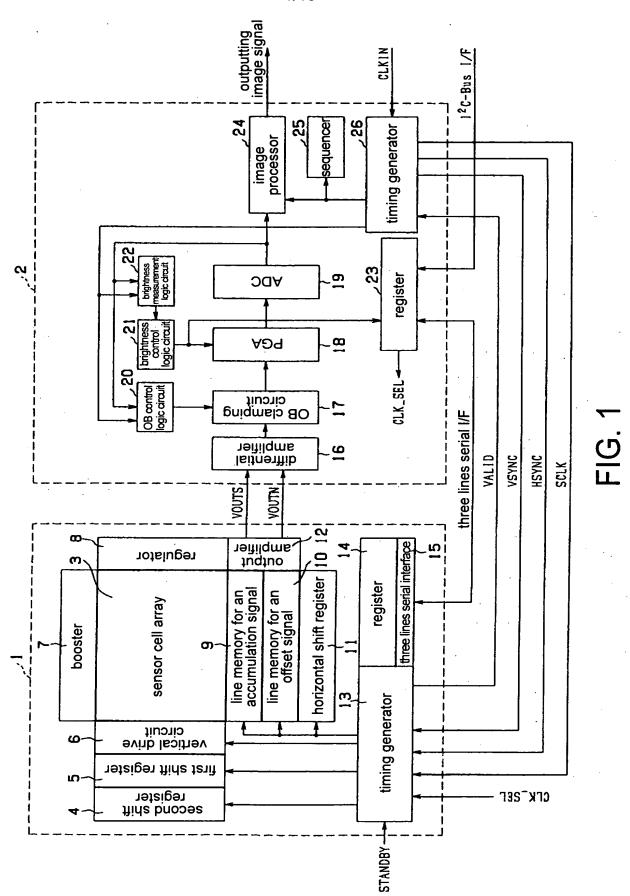
Title: IMAGE-PROCESSING DEVICE, IMAGE-PROCESSING METHOD AND SOLID-STATE

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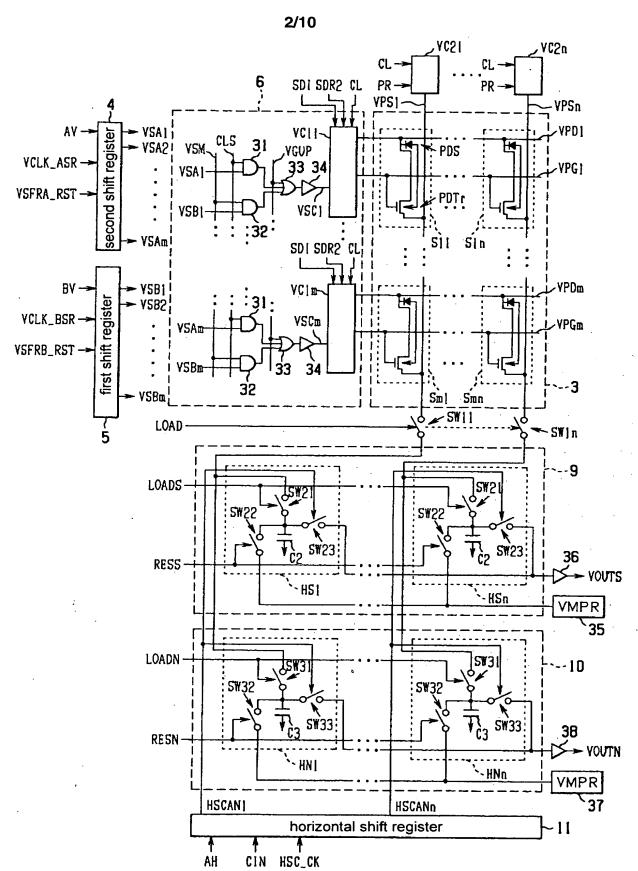


FIG. 2

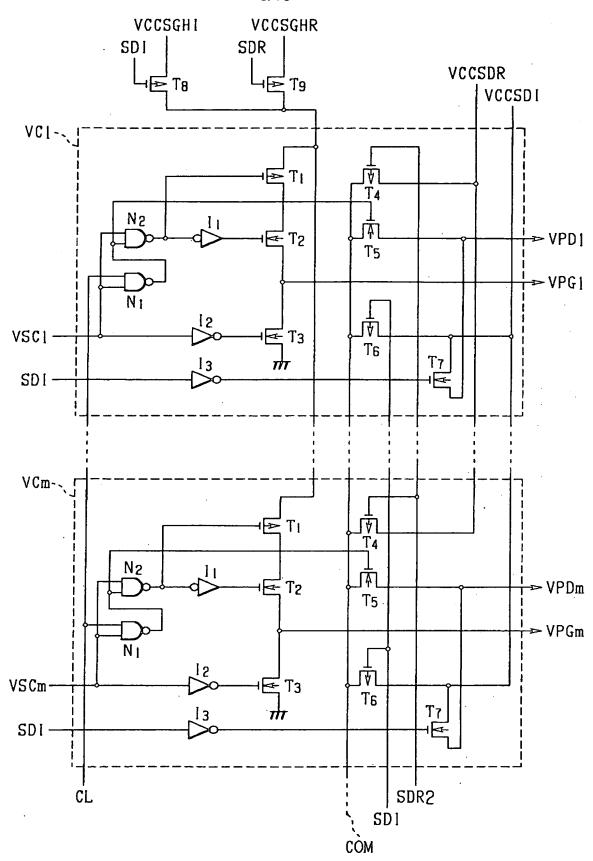


FIG. 3

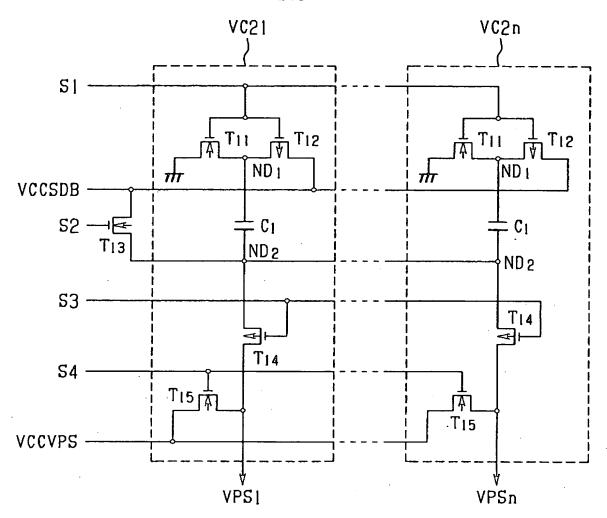


FIG. 4A

FIG. 4B

Title: IMAGE-PROCESSING DEVICE, IMAGE-PROCESSING METHOD AND SOLID-STATE IMAGE PICKUP DEVICE Inventor: Hiroaki NASU Atty. Ref.: 9319S-000596

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States of sensor bias	sensor t	oias							
	state	accumulation	RESS	LOADS	PR	73	RESN	LOADN	
GATE	selected	VCCSGHI	Lo	VCCSGHR	VCCSGHR	VCCSGHR+ VCCSDBX 2	Lo1	VCCSGHR	n
	non selected		Lo	Lo	Lo	Lo	Lo U	ر د	
SOURCE	SOURCE selected	turning "on" (VCCSD1)	VMPR	VCCSGHR-VthS	VCCVPS	VCCSDBX 2	VMPR	VCCSGHR-VthN	5/10
DRAIN	selected	VCCSDI	HiZ	VCCSDR	turning "on" (VCCVPS)	turning "on" (VCCSDB X 2)	HiZ	VCCSDR	<del>r</del>
	non selected		HiZ	VCCSDR	VCCVPS	VCCSDR	HiZ	VCCSDR	

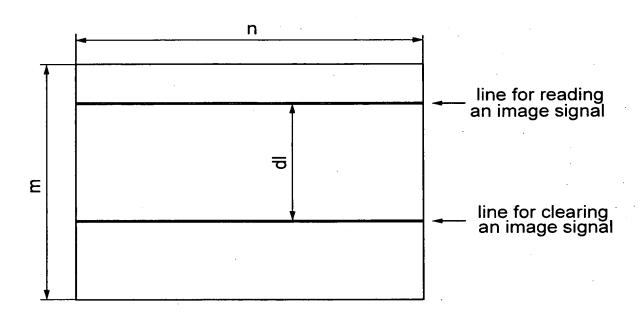
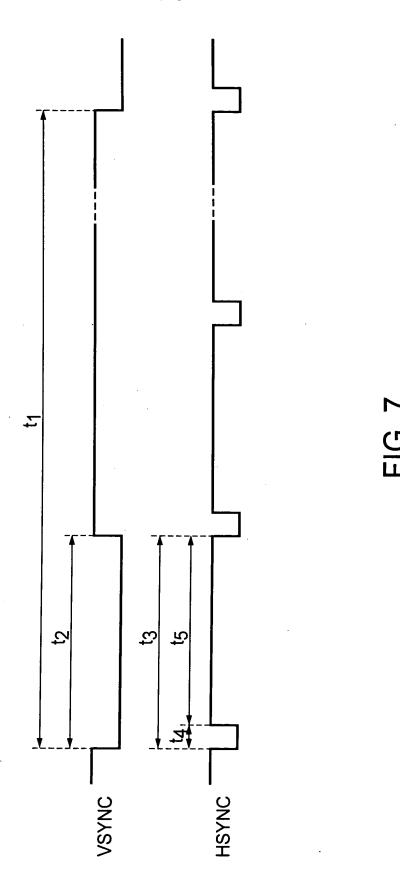


FIG. 6

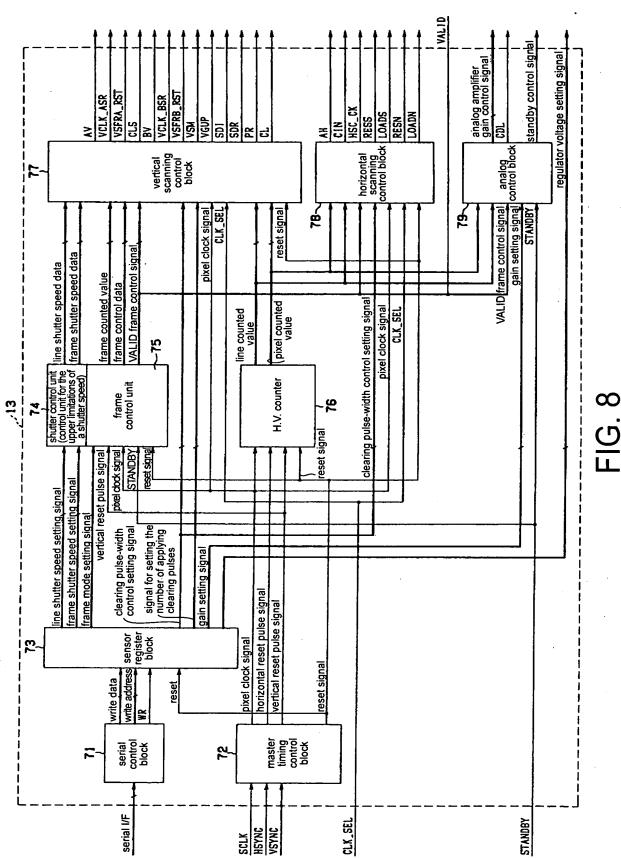
Inventor: Hiroaki NASU Atty. Ref.: 9319S-000596

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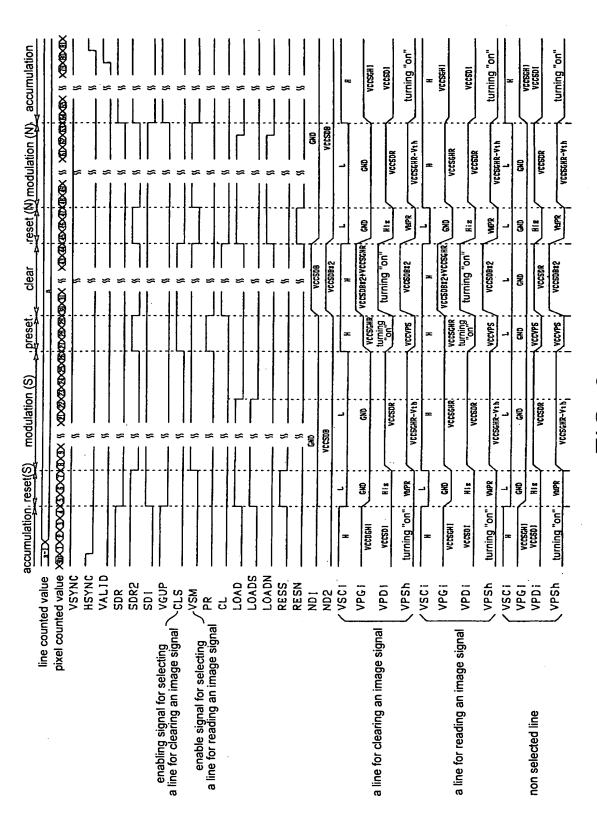
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Title: IMAGE-PROCESSING DEVICE, IMAGE-PROCESSING METHOD AND SOLID-STATE

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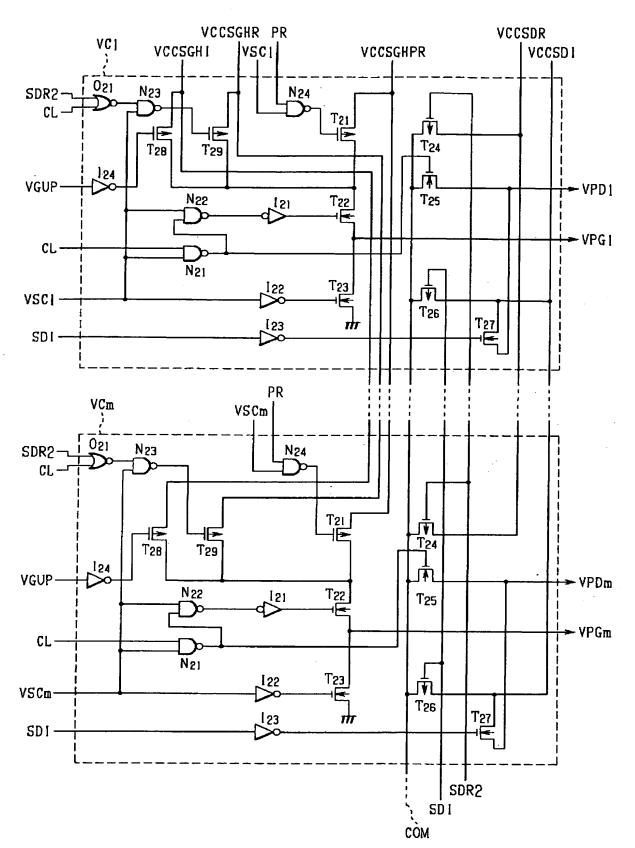


FIG. 10